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EDC Lab Manual - To Plot V-I Characteristics of Si/Ge Diode [CO1,2,3,4,5,6] 3 Prepared by: Prof. S. Irfan Ali Figure 1.3: V-I Characteristics PN Junction Diode THEORY: A PN junction is formed by diffusing P-type material to one half side and N-type material another half side. The plane dividing the two zones is known as a junction. FORWARD BIAS:

### **ELECTRONIC DEVICES AND CIRCUITS (EDC) LABORATORY MANUAL**

LAB MANUAL ELECTRONIC DEVICES & CIRCUITS LAB Dept. of ECE CREC 1 LAB MANUAL ON ELECTRONIC DEVICES & CIRCUITS LAB II B.TECH I SEMESTER ECE (JNTUA-R15) Verified and Compiled by: Dr. V. THRIMURTHULU, M.E., Ph.D., MISTE, MIETE Professor, Dept. of ECE Mr. S. ALI ASGAR, M.Tech ...

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1. To plot VI characteristic of junction diode and Zener diode. 2. For half wave rectifier with capacitor filter find line and load regulation and ripple factor.

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